

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	12013	(dc-dc adj convert\$3)	US-PGPUB ; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2004/11/16 14:42
L2	1102158	(integrated adj circuit or chip or ic)	US-PGPUB ; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2004/11/16 14:43
L3	2679	1 and 2	US-PGPUB ; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2004/11/16 14:43
L4	2405	3 and ((timing adj control) or control\$3)	US-PGPUB ; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2004/11/16 14:46
L5	167	4 and (feedback adj (terminal or circuit))	US-PGPUB ; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2004/11/16 14:57
L6	1	5 and scan\$3 adj process\$3	US-PGPUB ; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2004/11/16 14:57